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PATENT NUMBER

U.S. UTILITY Patent Application

PATENT DATE SCANNED

CONT/PRIOR CLASS SUBCLASS ART UNIT EXAMINER APPLICATION NO. 2133 09/994299 D 714 APPLICANTS

Miron Abramovici Charles Stroud

Identifying faulty programmable interconnect resources of field programmable gate arrays

ORIGINAL							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							
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TERMINAL	DRAWINGS			CLAIMS ALLOWED	
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
The term of this patent				NOTICE OF ALLOWANCE MAILED	
subsequent to (date)					
has been disclaimed.	(Assistant Examiner)		(Date)		
☐ The term of this patent shall				,	
not extend beyond the expiration date of U.S Patent. No.			ISSUE FEE		
				Amount Due	Date Paid
	(Primary Examiner)		(Date)		
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